



Institution of Electronics & Telecommunication Engineers (IETE) Student Chapter

The Institution of Electronics and Telecommunication Engineers (IETE) founded in 1953 is one of the leading Professional Society in India. With the great vision of founder and subsequent forefathers and stalwarts who were leading light of the Institution IETE, have been devoting and contributing for the advancement of Science and Technology in the fields of Electronics, Communication Engineering, Computer Science, Information Technology and other related subjects. The objectives of IETE focus on advancing electro-technology. The IETE conducts and sponsors technical meetings, conferences, symposia, and exhibitions all over India, publishes technical journals and provides continuing education as well as career advancement opportunities to its members. The IETE students' forum (ISF) is set up in the department, this year with the assistance and involvement of faculty members of the institution. The IETE centre in the region also provides support and coordination in the establishment and the working of the forum.

The Institution of Electronics & Telecommunication Engineers (IETE) Students' Chapter was started in the institute from the academic year 2021-22. This year the ISF membership count is increased to 128.



ISF Installation Ceremony



The Installation ceremony of IETE Students Forum (ISF) for the Academic Year 2022-23 was conducted on 15th November, 2022 at the hands of Mr. Santosh Kamble, Founder, Saitronics, Navi Mumbai. Mr. Kamble, guided the students towards increasing their technical skill set for a promising career in the world of work.

Dr. A. M. Agarkar, Principal, Govt. Residential Women's Polytechnic, Latur, and Chairman of the ISF Installation Ceremony, congratulated the newly appointed office bearers of ISF and wished the students to explore newer aspects in technological field. He encouraged the students to participate in various symposiums in large numbers and bag the advantage. Prof. A. S. Laturkar, Convener ISF & HOD (ET) also encouraged the students for increased involvement in ISF activities.



The ISF Core Committee



Activities Conducted under ISF

GOVERNMENT RESIDENTIAL WOMEN'S POLYTECHNIC, LATUR
Department of Electronics & Communication Engineering
Institution of Electronics & Telecommunication Engineers (IETE)
IETE Students Forum Presents

15th October, 2022

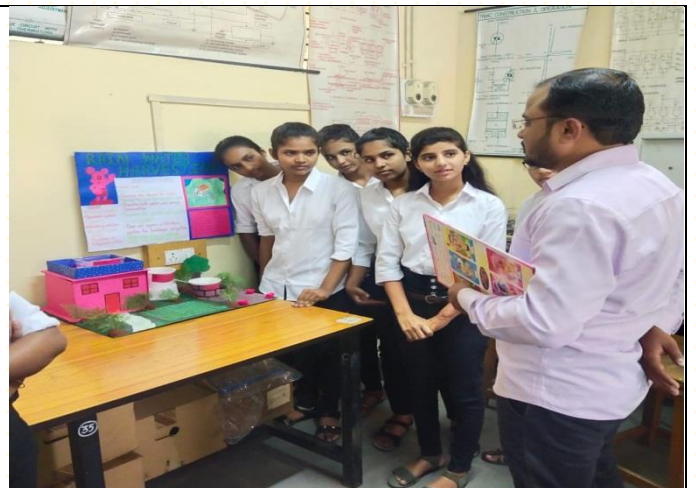
Model Competition
• 1 Year ET
• For more details
• Shravani Kalkote
• 7249584366

Technical Aptitude Test
• II ET
• For more details
• Sakshi Shelge
• 9356991973

Technical Paper Presentation
• III ET
• For more details
• Ankita Birajdar
• 7666004910

Registration Form Link : <https://forms.gle/HxwCCvpi8c4Uv8>

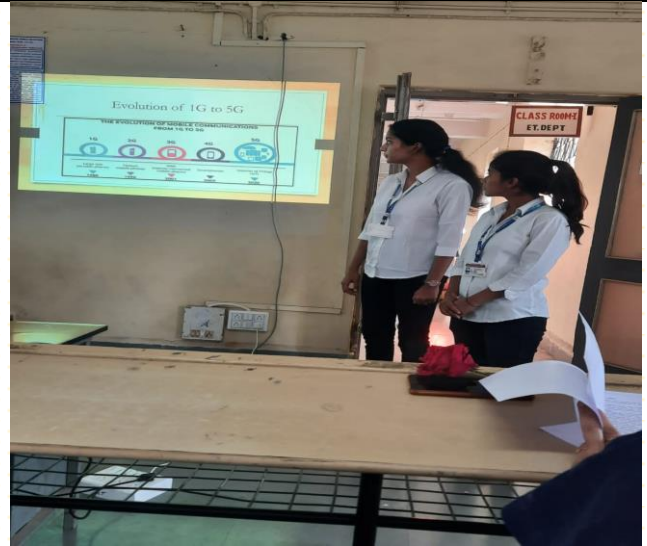
IETE Poster



Working Model Competition



Technical Aptitude Test



Technical Paper Presentation

Under IETE Students Forum (ISF), following activities were conducted for students –

i) Working Model Competition

This competition was conducted on 15th October, 2022 for first year students. Around 48 students participated in this event and presented a wide variety of working models of various scientific phenomena from volcano to satellite communication. Mrs. N. M. Jadhav, Lecturer (ET) and Mrs. K. G. Giri, Lecturer (ET) co-ordinated for the arrangements of this event. Mr. R. B. Kakde, Lecturer (EE), judged the models and provided them with a new dimension to think.

ii) Technical Aptitude Test

This competition was conducted on 15th October, 2022 for second year students. Around 71 students participated in this event. This event is an effort by the department to provide the students with all preparatory exposure for campus interviews. Mrs. A. S. Bhandare, Lecturer (ET) and Mrs. S. H. Chincholkar, Lecturer (ET) co-ordinated for the arrangements of this event. Ms. M. G. Mulge, Lecturer (CO), also helped for the smooth conduction of this event.

iii) Technical Paper Presentation

This competition was conducted on 01st December, 2022 for third year students. Around 10 students participated in this event. Students presented very well prepared technical papers on the recent advancements in the field of Electronics & Communication Engg. Mrs. S. A. Agarkar, Lecturer (ET) and Mrs. K. G. Giri, Lecturer (ET) co-ordinated for the arrangements of this event. Ms. S. A. Hartalkar, Lecturer (MU), judged the event and provided the students with all the necessary guidance.



Technical Workshop

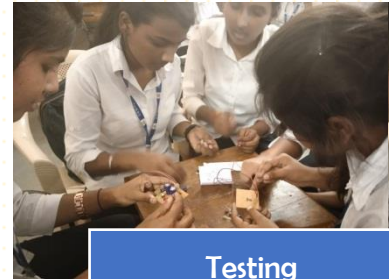
PCB Design and Manufacturing using Proteus



PCB Designing



Screen Printing



Testing



Theory Session



Certificate Distribution



Online Test

Mr. Santosh Kamble, Saitronics, Navi Mumbai successfully conducted a 03 days technical workshop on PCB Designing and Manufacturing using Proteus from 15th to 17th November, 2022. This session was attended by 53 students from Second year Electronics & Communication Engg. It was a thorough session on PCB Designing using various softwares like Proteus, Express PCB, TinkerCAD etc. The students were provided complete hands on experience of PCB development process. This workshop was an effort by the department to bridge the gap due to online teaching learning in the pandemic period. Mrs. K. G. Giri, Mrs. A. P. Ghode and Mrs. S. H. Chincholkar co-ordinated for all the necessary arrangements.